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BHARAT ELECTRONICS LIMITED

(A Govt. of India Enterprise under the Ministry of Defence)

PDIC & CoE

Advertisement No. 6050/TE-I&PE-I/HR/PDIC/2023-24, dated: 14.06.2023

Bharat Electronics Limited (BEL) a Navaratna Company and India's premier Professional Electronics Company under the Ministry of Defence, requires the following personnel **ON TEMPORARY BASIS** for its Product Development & Innovation Centre (PDIC) & Centres of Excellence (CoE), Bengaluru.

Sl. No.	Job Code	Post	No. of Posts	Minimum relevant Post Qualification Experience as on 01.05.2023	Upper Age limit as on 01.05.2023	Reservation	Consolidated remuneration per Month
1	TE0101	**Trainee Engineer-I** (Electronics/ Mechanical/ Computer Science)	Electronics-**33** Mechanical-**04** Computer Science-**06** Total- **43 posts**	Minimum **06 Months** of relevant post qualification industrial experience	28 Years	GEN-19 EWS-03 OBC-10 SC-07 ST-04	1st year - Rs. 30,000/- 2nd year - Rs. 35,000/- 3rd year - Rs. 40,000/-
2	PE0101	**Project Engineer-I** (Electronics/ Mechanical/ Computer Science)	Electronics-**24** Mechanical-**05** Computer Science-**09** Total- **38 posts**	Minimum **02 years** of relevant post qualification industrial experience	32 years	GEN-20 EWS-07 OBC-05 SC-04 ST-03	1st year - Rs. 40,000/- 2nd year - Rs. 45,000/- 3rd year - Rs. 50,000/- 4th year - Rs. 55,000/-
3	PE0102	**Project Engineer-I** (Control Electronics)	Electronics/ Electrical-**01** Total- **01 post**				

For PwBD (Person with Benchmark Disability) candidates, reservation will be as per Government guidelines.

1.0 TERMS OF ENGAGEMENT & ALLOWANCE:

a. Trainee Engineer - I:

Trainee Engineers will be engaged for an initial period of 02 years and may be extended upto a maximum of 01 year (including initial period) based on project requirement and individual performance. Candidates will be paid consolidated remuneration of Rs. 30,000/- per month for the 1st year and Rs. 35,000/- for 2nd year. In the event of extension of tenure, the remuneration shall be Rs. 40,000/- for 3rd year per month.

b. Project Engineers - I:

Project Engineers will be engaged for an initial period of 3 years which may be extended upto a maximum of 01 year (including initial period) based on project requirement and individual performance. Candidates will be paid a consolidated remuneration of Rs. 40,000/- per month for the 1st year and Rs. 45,000/- for 2nd year and Rs. 50,000/- for 3rd year. In the event of extension of tenure, the remuneration shall be Rs. 55,000/- for 4th year per month.

c. However, if any candidate has completed three years of tenure as a Trainee Engineer in BEL and applied and selected for Project Engineer post he/ she will be directly inducted as Project Engineer –II. The overall tenure will be limited to 3 years only. The remuneration of such candidates will be Rs.45.000/- for the 1st year, Rs. 50,000/- for 2nd year and Rs.55,000/- for the 3rd year.

d. In addition to above a consolidated amount of Rs. 12,000/- per year will be paid towards expenses like Medical Insurance & Life Insurance premium, attire allowance, stitching charges, footwear allowance etc. The additional amount shall be paid in two instalments. First instalment will be processed in the first month's salary after joining and the second instalment will be paid after completion of 6 months from the date of joining.

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2.0 Upper Age limit as on 01.05.2023:

a. Trainee Engineer - I:

The age should not be more than **28 years** for General & EWS candidates.

The upper age limit will be relaxation for OBC candidates by 03 years and SC/ST Candidates by 05 years. For Candidates belonging to PwBD category having minimum 40% disability or more will get 10 years relaxation in upper age limit in addition to the relaxation applicable to the categories mentioned above.

b. Project Engineers- I:

The age should not be more than **32 years** for General & EWS candidates.

The upper age limit will be relaxation for OBC candidates by 03 years and SC/ST candidates by 05 years. For candidates belonging to PwBD category having minimum 40% disability or more will get 10 years relaxation in upper age limit in addition to the relaxation applicable to the categories mentioned above.

Please Note:

a. Candidates seeking age relaxation will be required to enclose copy of necessary certificate along with application and submit in original in case of selection or provide at any subsequent stage of the process for verification.

a. The OBC/ EWS/ Category/ Disability Certificate should be strictly in the format available on the BEL website failing which, candidates will be considered under 'General' category, provided they are otherwise meeting all other criteria stipulated for General Candidates.

b. The application of the candidates not meeting the prescribed age criteria will be rejected/ cancelled without any prior intimation.

c. Candidates belonging to OBC category (Non-Creamy Layer) should produce the certificate issued on or after **01.04.2022** and candidates belonging to EWS must produce a valid Income and Asset Certificate.

3.0 EDUCATION QUALIFICATION:

3.1 Trainee Engineers- I: Electronics/ Mechanical/ Computer Science Engineering discipline

B.E./ B. Tech / B.Sc Engineering degree (4-year course) in relevant disciplines of **Electronics / Mechanical/ Computer Science** from reputed University/ Institution/ College with **55%** and above for General/OBC/EWS candidates & Pass Class for SC/ ST/ PwBD Candidates.

3.2 Project Engineers - I: Electronics/ Mechanical/ Computer Science Engineering discipline

B.E./ B. Tech/ B.Sc Engineering degree (4-year course) in relevant disciplines **Electronics/ Mechanical/ Computer Science** of from reputed University/ Institution/ College with **55%** and above for General/ OBC/ EWS candidates & Pass Class for SC/ ST/ PwBD Candidates.

3.3 Project Engineers - I: Electronics/ Electrical Engineering discipline

B.E./ B. Tech/ B.Sc Engineering degree (4-year course) in relevant disciplines **Electronics/ Electrical** of from reputed University/ Institution/ College with **55%** and above for General/ OBC/ EWS candidates & Pass Class for SC/ ST/ PwBD Candidates.

The Disciplines and subjects defined for the Engineering posts mentioned above are as follows:

QUALIFICATION	SUBJECT	DISCIPLINE
B.E/ B.Tech/ B.Sc Engineering Degree (4-Years Course)	Electronics	Electronics Electronics & Communication Electronics & Telecommunication Communication Telecommunication
	Mechanical	Mechanical Engineering
	Computer Science	Computer Science Computer Science & Engineering Computer Science Engineering.

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B.E/ B.Tech/ B.Sc Engineering Degree (4-Years Course)	Electronics/ Electrical	Electronics Electronics & Communication Electronics & Telecommunication Communication Telecommunication Electrical & Electronics Engineering Electrical Engineering

Please Note:

a. For all the posts indicated, the candidates should have qualified from AICTE/ UGC approved college/Institute or recognized University.

b. Candidates applying for the above indicated posts should possess 55% and above for General/ OBC/ EWS and Pass Class for SC/ ST/ PwBD.

c. Candidates have to compulsorily attach the Provisional/ Final Degree Certificate as proof of completion of Degree and indication of Class and percentage of marks.

d. In case any University/Institution or college is following an evaluation system of CGPA/ DGPA/ OGPA or letter grade, wherever applicable, it is mandatory for the candidate to submit **proof of Percentage (%) issued by university/Institution/College and awarded class along with the degree certificate**.

e. In case there is no mention of specialization in the qualifying degree as required in the minimum essential educational qualification candidates are required to submit a certificate from their University/Institution or college which indicates their specialization in qualifying degree.

f. Candidates application without the relevant prescribed documents mentioned will be rejected/ cancelled without any prior intimation.

4.0 RELEVANT POST QUALIFICATION INDUSTRIAL EXPERIENCE AS ON 01.05.2023:

4.1 Candidate(s) applying for JOB CODE - TE0101 (Trainee Engineer-I)

Candidate should have Minimum of **'SIX MONTHS'** of relevant post qualification industry experience as on 01.05.2023 for the post of **Trainee Engineer-I**

Electronics: Engineer with experience in design & development with knowledge/ competencies in some of the following areas:

- RF and Microwave: Basic RF circuits, Receivers (VLF, HF to microwave frequencies), Receiver front end design & testing, Passive and active components of RF receiver and Exciter chain, low noise amplifiers, Receivers, Exciters, Frequency synthesizers, low and medium Power amplifiers, combiners, splitters. Simulation and design of narrow, and wide Band antennas starting from HF band. Omni and Directional antennas, Multi-band antennas. Familiarity on usage of any RF simulation tool will be an advantage.

- Power Electronics: Experience in PCB design tools like Allegro/PADS/Altium and familiarity with MATLAB tool. Experience in design and development of digital and analog electronics for power converters, voltage/current sensing and signal conditioning, Op-amp based circuit, design of power electronics products using high power MOSFETs for Battery Chargers, AC-DC converters, DC-DC Converters.

- Embedded Products: Design of Embedded Systems with 16/32/64 bit X86/RISC/ARM/DSP Processor, with experience in Microcontroller, FPGA based hardware design, VHDL /Verilog Coding, familiarity with Vivado or equivalent tool and testing. SOC/FPGA based board Design, C, C++, Embedded Linux, High speed Transceivers, Memories (DDR3, DDR4, QDR), Ethernet (1G/10G,100G), PCIe, High speed ADC/DAC.

- Experience in development of Device Drivers, BSP Development in ARM, PowerPC etc with Windows/Linux/VXWorks operating systems with knowledge in debugging tools & emulators. Experience in kernel programming like Memory Mgmt, Threads/Processes, IPCs, Synchronization, Interrupts, Boot Sequence etc. will be an added advantage. Knowledge of buses & external Interfaces (VPX, PCIe, 1553, ARINC…). Working Knowledge in FPGA based SoC will also be an added advantage.

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- Communication Products: Knowledge of Networking Protocols, SIP protocols, Voice Media processing (Audio, Video Codec), Software Defined Networks, Wireless Networking Protocols, 4G, 5G, IP Multi Media Services (IMS), Digital Modulations, satellite communications, electro-optics design.
- Digital Electronics domain: Microcontroller/DSPs/ FPGA based hardware design with High speed Transceivers/ Memories (DDR3, DDR4, QDR)/ Ethernet (1G/10G,100G) and other interfaces like SPI/UART/I2C/GPIO. Experience in Embedded C/VHDL /Verilog programming languages, digital and analog electronics-based mixed signal design and testing and board bring up. Familiarity with Vivado or equivalent tool, PCB design tools like allegro, OrCAD tool will be an added advantage.
- Familiarity on usage of any RF simulation tools like ADS/ System Vue/Microwave Office AWR will be an advantage.
- Communication domain: Knowledge of Digital Modulation design and testing, knowledge of Wired/Wireless Networking Protocols, SIP protocols, knowledge of MATLAB or similar simulation tools. Awareness of Radio communication and satellite communication will be an added advantage.

Mechanical:

- Candidate should have experience in modeling and design of mechanical parts/ housings, product engineering, thermal and structural analysis of electronic product enclosures using any 3D software & analysis tool. Should have experience in engineering documentation and any ERP software like SAP. Knowledge of tools like Solid Works, Auto CAD, CATIA, ANSYS or similar tools for product design will be preferred. Should also have knowledge on materials for product design for various requirements including defence applications.
- Tools: Exposure and usage of Auto CAD/Solid Works, ANSYS, CATIA or similar tools
- Modeling: Exposure in 3D modeling tools, DXF/DWG modeling of PCBs and Electronic systems
- Design: Exposure to Product Development of electronics systems, Thermal Simulation& Analysis, PCB Modeling, 3D modeling.

Computer Science:

- Candidate should have experience in Driver development on ARM based Micro-controller, BSP & Device driver development in Linux Operating System or any RTOS and Porting to custom hardware, Development of APK's (Android Application) using the Android Studio.
- Usage of Languages like C, Embedded C, C++, JAVA 2, J2EE, Javascript, PHP, CSS, HTML, python. Usage of software development /debug tools like Qt, Net Beans, Eclipse, Visual Studio code, Spring too suite, Brackets and memory management techniques.
- Knowledge on Framework like Angular JS, React JS, Spring, Hibernate, Spring boot is preferred.
- Domain Experience in Front End Development, Backend development, Full stack development GUI development using Java, QT. Knowledge of interface protocol like I2C, SPI, etc.
- Working experience in I/O interface development like UART, GPIO, Ethernet, ADC, etc.
- Experience in Boot Loader Concepts, Uboot, Customization and porting on hardware, Bare Metal Coding for Micro-controllers. Knowledge on AI, Machine Learning algorithms, Neural Networks, Robotics Automation will also be preferred.
- Languages: Knowledge in Embedded C/C/C++
- OS: Exposure to RTOS/Linux porting
- Software: Exposure in development of BSP/Device Drivers/Application Development, Software for Microcontrollers/DSPs based platforms
- Protocols: Knowledge in TCP/IP, L2/L3 Layers processing.

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4.2 Candidate(s) applying for JOB CODE - PE0101 (Project Engineer-I)

Candidate should have Minimum of **'TWO YEARS'** of relevant post qualification industry experience as on 01.05.2023 for the post of **Project Engineer-I**

Electronics: Engineer with experience in design & development with knowledge/ competencies in some of the following areas:

- Digital Electronics domain: Microcontroller/DSPs/ FPGA based hardware design with High speed Transceivers/ Memories (DDR3, DDR4, QDR)/ Ethernet (1G/10G,100G) and other interfaces like SPI/UART/I2C/GPIO. Experience in Embedded C/VHDL /Verilog programming languages, digital and analog electronics-based mixed signal design and testing and board bring up. Familiarity with Vivado or equivalent tool, PCB design tools like allegro, OrCAD tool will be an added advantage.

- RF and Microwave domain: Basic RF circuits, Receivers (VLF, HF to microwave frequencies), Receiver front end design & testing, Passive and active components of RF receiver and Exciter chain, low noise amplifiers, Receivers, Exciters, Frequency synthesizers, low and medium Power amplifiers, combiners, splitters. Familiarity on usage of any RF simulation tools like ADS/ System Vue/Microwave Office AWR will be an advantage.

- Communication domain: Knowledge of Digital Modulation design and testing, knowledge of Wired/Wireless Networking Protocols, SIP protocols, knowledge of MATLAB or similar simulation tools. Awareness of Radio communication and satellite communication will be an added advantage.

- RF and Microwave: Basic RF circuits, Receivers (VLF, HF to microwave frequencies), Receiver front end design & testing, Passive and active components of RF receiver and Exciter chain, low noise amplifiers, Receivers, Exciters, Frequency synthesizers, low and medium Power amplifiers, combiners, splitters. Simulation and design of narrow, and wide Band antennas starting from HF band. Omni and Directional antennas, Multi-band antennas. Familiarity on usage of any RF simulation tool will be an advantage.

- Power Electronics: Experience in PCB design tools like Allegro/PADS/Altium and familiarity with MATLAB tool. Experience in design and development of digital and analog electronics for power converters, voltage/current sensing and signal conditioning, Op-amp based circuit, design of power electronics products using high power MOSFETs for Battery Chargers, AC-DC converters, DC-DC Converters.

- Embedded Products: Design of Embedded Systems with 16/32/64 bit X86/RISC/ARM/DSP Processor, with experience in Microcontroller, FPGA based hardware design, VHDL /Verilog Coding, familiarity with Vivado or equivalent tool and testing. SOC/FPGA based board Design, C, C++, Embedded Linux, High speed Transceivers, Memories (DDR3, DDR4, QDR), Ethernet (1G/10G,100G), PCIe, High speed ADC/DAC.

- Experience in development of Device Drivers, BSP Development in ARM, PowerPC etc with Windows/Linux/VXWorks operating systems with knowledge in debugging tools & emulators. Experience in kernel programming like Memory Mgmt, Threads/Processes, IPCs, Synchronization, Interrupts, Boot Sequence etc. will be an added advantage. Knowledge of buses & external Interfaces (VPX, PCIe, 1553, ARINC…). Working Knowledge in FPGA based SoC will also be an added advantage.

- Communication Products: Knowledge of Networking Protocols, SIP protocols, Voice Media processing (Audio, Video Codec), Software Defined Networks, Wireless Networking Protocols, 4G, 5G, IP Multi Media Services (IMS), Digital Modulations, satellite communications, electro-optics design.

Mechanical:

- Tools: Experience in Auto CAD/Solid Works, ANSYS, CATIA or similar tools
- Modeling: Experience in 3D modeling tools, DXF/DWG modeling of PCBs and Electronics enclosures
- Design: Product Development of electronics systems, Product Design for EMI/EMC, Thermal Simulation& Analysis, Structural Simulation& Analysis for Electronics systems, Design of VPX/VME based systems, PCB Models design, 3D modeling, QC/QA test reports and documentation.

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- Candidate should have experience in modeling and design of mechanical parts/ housings, product engineering, thermal and structural analysis of electronic product enclosures using any 3D software & analysis tool. Should have experience in engineering documentation and any ERP software like SAP. Knowledge of tools like Solid Works, Auto CAD, CATIA, ANSYS or similar tools for product design will be preferred. Should also have knowledge on materials for product design for various requirements including defence applications.

Computer Science:

- Languages: Experience in Embedded C/C/C++
- OS: Porting of U-boot& boot loader in embedded platforms, RTOS/Linux/Integrity or similar OS porting
- Software: Experience in Embedded software for Microcontrollers/DSPs/MPSoC based platforms, BSP/Device Drivers/Application Development, Knowledge of H/W interfaces (SPI/UART/I2C/GPIO etc.,), Exposure to QT Applications, Software processing of Interrupts/Exceptions/Multithread/Real-time Processing etc.,
- Protocols: Experience in TCP/IP, Networking/Routing protocols, L2/L3 Layer processing etc.,
- Candidate should have experience in Driver development on ARM based Micro-controller, BSP & Device driver development in Linux Operating System or any RTOS and Porting to custom hardware, Development of APK's (Android Application) using the Android Studio.
- Usage of Languages like C, Embedded C, C++, JAVA 2, J2EE, Javascript, PHP, CSS, HTML, python. Usage of software development /debug tools like Qt, Net Beans, Eclipse, Visual Studio code, Spring too suite, Brackets and memory management techniques.
- Knowledge on Framework like Angular JS, React JS, Spring, Hibernate, Spring boot is preferred.
- Domain Experience in Front End Development, Backend development, Full stack development GUI development using Java, QT. Knowledge of interface protocol like I2C, SPI, etc.
- Working experience in I/O interface development like UART, GPIO, Ethernet, ADC, etc.
- Experience in Boot Loader Concepts, Uboot, Customization and porting on hardware, Bare Metal Coding for Micro-controllers. Knowledge on AI, Machine Learning algorithms, Neural Networks, Robotics Automation will also be preferred.

4.2 Candidate(s) applying for JOB CODE - PE0102 (Project Engineer-I)

Candidate should have Minimum of **'TWO YEARS'** of relevant post qualification industry experience as on 01.05.2023 for the post of **Project Engineer-I**

Electronics/ Electricals:

- Understanding of FPGAs/ Controllers/ DSPs
- Experience on mechanical components like Resolvers / Encoders, Gyros, accelerometers etc.
- Development of Algorithms and software for implementing Control loops (PID) to stabilize the platform
- Experience of Signal Processing
- Experience of MATLAB
- Experience of communication protocols for interfacing with various components of gimbal system like RS 232, RS422, USB, LAN, CAN Bus etc.
- VHDL /C Programming

Please Note:

a. Candidates possessing ***post qualification industry experience*** only need to apply.

b. **Teaching/ Academic/ Research work will not be considered as relevant post qualification industrial experience. Experience in banking and financial institutions, non-profit organizations and, internship placements which are a part of academic curriculum will not be considered as experience. Work experience prior to completion of BE/ B.Tech/ B.Sc will also not qualify as post qualification experience.**

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c. Candidate should possess Experience certificates/ Documents issued by the previous and present employer clearly indicating period of employment and post held.

d. The decision of the Selection Committee with respect to industrial experience will be final. Work experience indicated without supporting documents will not be considered and is liable to be rejected/ cancelled without any prior intimation.

5.0 SELECTION PROCEDURE:

a. Candidates meeting the specified criteria as mentioned above in the advertisement will be communicated to attend the Written Test for **85 marks**.

b. Candidates will be short-listed for the interviews in the order of merit based on the performance in the written test, in the ratio of 1:5. Interviews will be conducted subsequently and will be for 15 marks.

c. The list of candidates shortlisted for the Written Test/ Interview, will be hosted on the BEL Website. Candidates are required to comply with the instructions indicated in the Admit Card.

Please Note:

- Candidates have to compulsorily provide the CGPA conversion to percentage of marks, as per the guidelines provide by the concerned University. Failure to do so, will result in disqualification.
- Only relevant ***post qualification industry experience*** will be considered. The decision of the selection committee, with respect to relevant of experience will be final. Work experience indicated without supporting documents, will not be considered.
- Intimation w.r.t conducting of the written test and subsequently the Interviews will be sent through email provided by the candidates. Candidates are required to comply with the instructions indicated therein. The results of the final selection will be made available on BEL website.

6.0 METHOD OF SELECTION:

Selection will be through a Written Test for shortlisted candidates, followed by an Interview, only for those candidates who qualify in the Written Test. **The venue for the Written Test/ Interview will be at Bengaluru**.

7.0 APPLICATION FEES:

a. Trainee Engineer: Candidates belonging to General/ OBC/ EWS category are required to pay ***Rs. 150/- plus 18% GST*** as an application fee.

b. Project Engineer: Candidates belonging to General/ OBC/ EWS category are required to pay ***Rs. 400/- plus 18% GST*** as an application fee.

c. Candidates belonging to SC, ST & PwBD categories are exempted from payment of application fee.

a. Candidates are requested to read the details and screenshots for making the payment.

b. Candidates can also make the payment by approaching SBI branch. Candidates are required to select SBI branch in the payment option and download & print the challan generated through SBI Collect and deposit the application fee of Trainee Engineer-I of Rs. 150/- plus 18% GST and Project Engineer-I of Rs. 400/- plus 18% GST applicable bank charges in any SBI Branch. The candidate should ensure to obtain the seal and signature of the bank official.

c. Candidates have to enter the "**SBI Collect Reference No.**" generated after payment, in the Application Form.

d. Candidate may go through all instructions and eligibility criteria carefully before remitting application fees and sending the application. **Fees once paid will not be refunded under any circumstances.** Candidates may take note that no cheque, DD or cash will be accepted towards payment of application fee.

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INSTRUCTION FOR MAKING PAYMENT:

a. Go to www.onlinesbi.com and select: - State Bank Collect.
b. Accept Terms and Conditions and click on "**Proceed"**.
c. Select state of Corporation/ Institution: - All India.
d. Select type of Corporation/ Institution: - PSU – Public Sector Undertaking and Click on "**Go"** option.
e. Select PSU - Public Sector Undertaking: - Bharat Electronics Limited and Click on "**Submit"** button.
f. Select Payment category: -

Trainee Engineer - "Recruitment of Trainee Engineer for PDIC"
Project Engineer - "Recruitment of Project Engineer for PDIC"

g. Complete the payment as explained above
h. Take a print of the payment receipt and attach it with the application form.

Note: While paying application fees through SBI collect, candidates should ensure that they mention the same mobile number and email id in SBI collect as has been mentioned by them in the application form.

In case a candidate applies for both posts, Trainee Engineer and Project Engineer the application fee should be remitted separately for each post.

8.0 INTERESTED CANDIDATES MEETING ALL THE CRITERIA MENTIONED ALOFT, SHOULD SUBMIT THE APPLICATION IN THE FORMAT APPENDED TO THIS ADVERTISEMENT AND SEND THE APPLICATIONS THROUGH POST, SUPER-SCRIBING ON THE ENVELOPE THE POST APPLIED FOR AND FOLLOWING DOCUMENTS SHOULD COMPULSORILY BE ENCLOSED:

a. Duly filled-in & signed application form. (Downloaded from the BEL website)
b. Passport size photo
c. SSC/ SSLC/Matriculation Certificate for proof of Age.
d. Final Degree Certificate/ Provisional Degree Certificate of relevant qualification.
e. Proof of norms for CGPA/DGPA/OGPA or letter grade/document for percentage/class issued by University/Institution/College.
f. Post qualification industrial experience certificate/s from previous to till current employer. The joining/appointment letter and relieving letter (wherever applicable) needs to be attached to determine the number of years of post-qualification industrial experience.
g. A separate write up mentioning the details of the post qualification industrial experience.
h. Category/ Tribe/ Community/ Disability/ Economic status certificate in case of candidates belonging to SC/ ST/ OBC/ PwBD/ EWS respectively. Candidates claiming reservation under any of the above categories are required to submit the certificate in the prescribed format. The formats of various certificates are provided as link to the advertisement. Candidates belonging to OBC category should produce the certificate issued on or after 01.04.2022 and candidates belonging to EWS must produce a valid Income and Asset Certificate.
i. Candidates employed on Permanent/Fixed term basis in Government/ Quasi Government and Public Sectors undertaking should compulsorily submit the **'No Objection Certificate'** along with the application.
j. Candidates employed on temporary basis in Government/ Quasi Government and Public Sectors undertaking should compulsorily submit Offer of Appointment and latest pay slip.
k. Proof of identity - Aadhar/ Voter ID/ Driving License/ Any other Govt. approved identity document.
l. **The Trainee Engineers who are currently working in BEL prior intimation shall be given to the respective HR department of the Unit/ SBU/ CSG where the engineer is working. Copy of the same duly signed by concerned HR to be submit along with application.**
m. Copy of the SBI receipt.

9.0 GENERAL INSTRUCTIONS:

a. Only India nationals need to apply for the posts.
b. Candidates are advised to carefully read the full advertisement for details of eligibility criteria and selection modalities before submission of application.

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c. **The number of posts indicated above, may vary based on the actual requirement at the time of selection. Application that are incomplete, not in prescribed format, without the required enclosures, not meeting the criteria as specified in the advertisement, will be summarily rejected without assigning any reason and no correspondence in this regard will be entertained. There will be no separate communication to any candidates on their non-selection at any stage.**

d. The candidates should be willing to travel extensively across the country based on the project requirement. Candidates may be posted anywhere in India at any point of time. Hence, Candidates who are desirous of applying for the posts should be willing to relocate based on the project requirement across India.

e. Application of the candidate without fee payment (except for SC/ ST/ PwBD), will be rejected/cancelled without any prior intimation.

f. All information submitted in the application will be verified with the original documents at the time of document verification or at the stage of the recruitment process. If any documents/information provided by the candidate is found to be false or incorrect or Non-conformity with the eligibility criteria, then his/her candidature is liable to be rejected/cancelled at any stage of the recruitment and selection process without any prior intimation.

g. Candidature is liable to be rejected at any stage of recruitment/ eligibility/ selection process or after joining, if any information provided by the candidate is not found in conformity with criteria mentioned in the advertisement. The candidature is also liable for rejection at any stage if BEL comes across any evidence/knowledge that the qualification, experience and any other particulars indicated in the application/ other forms/ formats are not recognized/ false/ misleading and/or amounts to suppression of information/ particulars, which should have been brought to the notice of BEL.

h. Mere submission of application does not guarantee the adequacy of the candidature for being considered for the selection process.

i. Decision of BEL in all matters regarding eligibility of the candidate, the stages at which such scrutiny of eligibility is to be undertaken, qualification and other eligibility norms will be final and binding on the candidate.

j. Relaxation and Concession will be applicable to the candidates belonging to SC/ ST/ OBC/ PwBD/ EWS as per the Government directives.

k. Applicants should have sound health. No relaxation in health standard is allowed. Appointment of selected candidates will be subject to submitting Medical Fitness Certificate from a Medical Officer not below the rank of Civil Surgeon/ Assistant Surgeon at the time of joining.

l. Selected candidates are required to obtain a **Medical Insurance cover for a sum of Rs. 2 lakhs per annum for and Rs. 5 lakhs per annum for Life Cover Insurance** and the same should be submitted at the time of joining.

m. BEL reserves the right to debar/ disqualify any candidate at any stage of the selection process for any reason what so ever.

n. Management reserves the right to cancel/restrict/enlarge/modify/alter the recruitment/selection process, if need so arises, without issuing any further notice or assigning any reason thereafter.

o. Canvassing in any form will result in disqualification.

p. **Lateral application of Project Engineers/ Trainee Engineers for the same post is not permitted i.e. Project Engineers who is working in one of the units of BEL is not eligible to apply for Project Engineers, & Trainee Engineers who is working in one of the units of BEL is not eligible to apply for Trainee Engineers elsewhere within BEL. If applied, the application will be summarily rejected without assigning any reasons.**

Applications complete in all respects should be sent by post (**sealed envelope-A4 size only**) -
Super-scribed for Trainee Engineer "**Application for the post of Trainee Engineer (Job Code No.: _____)**"
Super-scribed for Project Engineer "**Application for the post of Project Engineer (Job Code No.: _____)**" and sent to below mentioned address:

Manager (HR),
Product Development & Innovation Centre (PDIC),
Bharat Electronics Limited,
Prof. U R Rao Road, Near Nagaland Circle,
Jalahalli Post, Bengaluru – 560 013, India.

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The application should reach the given address on or before **28.06.2023 by 5.00 PM**, application(s) sent through e-mail / any other channel will not be considered, any applications received after the closing date will not be considered, applications that are incomplete, not in the prescribed format, not eligible, without the required enclosures will be summarily rejected without assigning reasons and no correspondence in this regard will be entertained. BEL will not be responsible for any delay or loss in postal transit for any reason whatsoever.

There will be no separate communication to any candidates on their non-selection at any stage.

For clarification if any in respect of the above advertisement, you may write to **hrpdicrec@bel.co.in** or contact on 080-2219 5211.

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